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| U.S. UTILITY Patent Application | |
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| LE APPL | METHOD AND DEVICE FOR SYSTEM SIMULATION OF MICROCONTROLLERS/MICROPROCESSORS AND CORRESPONDING PERIPHERAL MODULES | | | | | | |
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ISSUING CLASSIFICATION

ORIGINAL CROSS REFERENCE(S)

CLASS SUBCLASS CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

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| TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | | |
|--|--|---|---------------------|---------------------------------------|-----------------------------|--|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G | |
| ☐ The term of this patent | | | | NOTICE OF AL | LOWANCE MAILED | |
| subsequent to (date) has been disclaimed. | (Assistant | Examiner) | (Date) | | | |
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| WARNING: The information disclosed herein may be res Possession outside the U.S. Patent & Traden | tricted. Unauthorized nark Office is restricted | disclosure may be to authorized emp! | prohibited by the U | United States Code Title 3 tors only. | 5, Sections 122, 181 and 36 | |
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